

Sheet 1 of 1

INFORMATION DISCLOSURE  
CITATION

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SERIAL NO.

2380-152

09/712,123

APPLICANT

HORLIN et al

FILING DATE

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## U.S. PATENT DOCUMENTS

\*EXAMINER  
INITIAL

DOCUMENT NUMBER

DATE

NAME

CLASS

SUBCLASS

FILING DATE  
IF APPROPRIATE

5,537,400

7-1996

Diaz et al.

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## FOREIGN PATENT DOCUMENTS

DOCUMENT

DATE

COUNTRY

CLASS

SUBCLASS

TRANSLATION

00/38380

6-2000

WO

YES

NO

## OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

- h Mirfakhraei, "Wafer-Scale Integration as a Technology Choice for High Speed ATM Switching Systems", Proceedings of the Annual International Conference on Wafer Scale Integration (from 1996 Proceedings of the Int'l. Conference on Innovative Systems in Silicon), US, New York, IEEE, vol. CONF. 6, 1994, pp. 282-291
- r Doi et al, "A High-Speed ATM Switch Architecture for FTTH - An ATM Switch Architecture with Input and Cross-Point Buffers", Proceedings of the International Switching Symposium, DE, Berlin, VDE Verlag, vol. SYMP. 15, 23 April 1995, pp. 384-388

\*Examiner

Date Considered

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Form PTO-FB-A820 (Also PTO-144)

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